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☐ 1.   Document ID: US 6334176 B1

L2: Entry 1 of 30   File: USPT   Dec 25, 2001

US-PAT-NO: 6334176
DOCUMENT-IDENTIFIER: US 6334176 B1

TITLE: Method and apparatus for generating an <u>alignment control vector</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
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☐ 2.   Document ID: US 6266758 B1

L2: Entry 2 of 30   File: USPT   Jul 24, 2001

US-PAT-NO: 6266758
DOCUMENT-IDENTIFIER: US 6266758 B1

TITLE: <u>Alignment and ordering of vector</u> elements for single instruction multiple data processing

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

☐ 3.   Document ID: US 6223320 B1

L2: Entry 3 of 30   File: USPT   Apr 24, 2001

US-PAT-NO: 6223320
DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

☐ 4.   Document ID: US 6085275 A

L2: Entry 4 of 30   File: USPT   Jul 4, 2000

US-PAT-NO: 6085275
DOCUMENT-IDENTIFIER: US 6085275 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
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☐ 5. Document ID: US 5996057 A

L2: Entry 5 of 30   File: USPT   Nov 30, 1999

US-PAT-NO: 5996057
DOCUMENT-IDENTIFIER: US 5996057 A

TITLE: Data processing system and method of permutation with replication within a vector register file

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image

☐ 6. Document ID: US 5961628 A

L2: Entry 6 of 30   File: USPT   Oct 5, 1999

US-PAT-NO: 5961628
DOCUMENT-IDENTIFIER: US 5961628 A

TITLE: Load and store unit for a vector processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image

☐ 7. Document ID: US 5933650 A

L2: Entry 7 of 30   File: USPT   Aug 3, 1999

US-PAT-NO: 5933650
DOCUMENT-IDENTIFIER: US 5933650 A

TITLE: Alignment and ordering of vector elements for single instruction multiple data processing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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☐ 8. Document ID: US 5922066 A

L2: Entry 8 of 30   File: USPT   Jul 13, 1999

US-PAT-NO: 5922066
DOCUMENT-IDENTIFIER: US 5922066 A

TITLE: Multifunction data aligner in wide data width processor

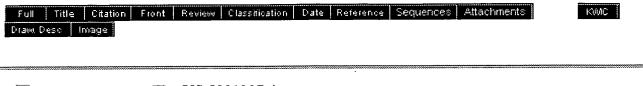| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐   9.   Document ID: US 5887183 A

L2: Entry 9 of 30   File: USPT   Mar 23, 1999

US-PAT-NO: 5887183
DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for <u>loading and storing vectors</u> in a plurality of modes

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐   10.   Document ID: US 5881307 A

L2: Entry 10 of 30   File: USPT   Mar 9, 1999

US-PAT-NO: 5881307
DOCUMENT-IDENTIFIER: US 5881307 A

TITLE: Deferred store data read with simple anti-dependency pipeline inter-lock control in superscalar processor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐   11.   Document ID: US 5812147 A

L2: Entry 11 of 30   File: USPT   Sep 22, 1998

US-PAT-NO: 5812147
DOCUMENT-IDENTIFIER: US 5812147 A

TITLE: Instruction methods for performing data formatting while moving data between memory and a vector register file

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐   12.   Document ID: US 5805874 A

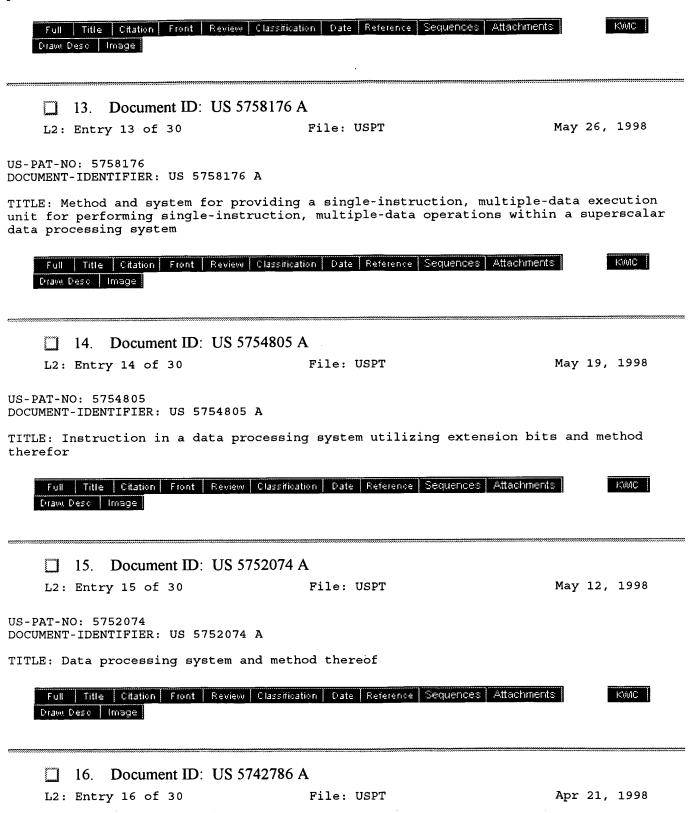L2: Entry 12 of 30   File: USPT   Sep 8, 1998

US-PAT-NO: 5805874
DOCUMENT-IDENTIFIER: US 5805874 A

TITLE: Method and apparatus for performing a vector skip instruction in a data
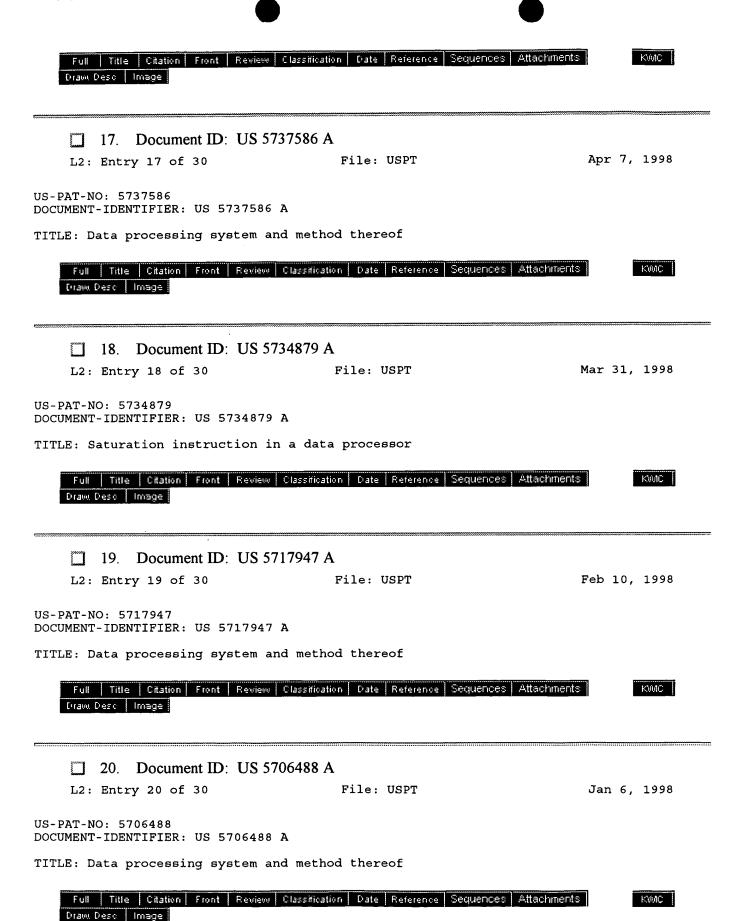
processor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   13.   Document ID: US 5758176 A

L2: Entry 13 of 30   File: USPT   May 26, 1998

US-PAT-NO: 5758176
DOCUMENT-IDENTIFIER: US 5758176 A

TITLE: Method and system for providing a single-instruction, multiple-data execution unit for performing single-instruction, multiple-data operations within a superscalar data processing system

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   14.   Document ID: US 5754805 A

L2: Entry 14 of 30   File: USPT   May 19, 1998

US-PAT-NO: 5754805
DOCUMENT-IDENTIFIER: US 5754805 A

TITLE: Instruction in a data processing system utilizing extension bits and method therefor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   15.   Document ID: US 5752074 A

L2: Entry 15 of 30   File: USPT   May 12, 1998

US-PAT-NO: 5752074
DOCUMENT-IDENTIFIER: US 5752074 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   16.   Document ID: US 5742786 A

L2: Entry 16 of 30   File: USPT   Apr 21, 1998

US-PAT-NO: 5742786
DOCUMENT-IDENTIFIER: US 5742786 A

TITLE: Method and apparatus for storing vector data in multiple non-consecutive locations in a data processor using a mask value

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |

| Draw Desc | Image |

☐ 17.   Document ID: US 5737586 A

L2: Entry 17 of 30   File: USPT   Apr 7, 1998

US-PAT-NO: 5737586
DOCUMENT-IDENTIFIER: US 5737586 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |

| Draw Desc | Image |

☐ 18.   Document ID: US 5734879 A

L2: Entry 18 of 30   File: USPT   Mar 31, 1998

US-PAT-NO: 5734879
DOCUMENT-IDENTIFIER: US 5734879 A

TITLE: Saturation instruction in a data processor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |

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☐ 19.   Document ID: US 5717947 A

L2: Entry 19 of 30   File: USPT   Feb 10, 1998

US-PAT-NO: 5717947
DOCUMENT-IDENTIFIER: US 5717947 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |

| Draw Desc | Image |

☐ 20.   Document ID: US 5706488 A

L2: Entry 20 of 30   File: USPT   Jan 6, 1998

US-PAT-NO: 5706488
DOCUMENT-IDENTIFIER: US 5706488 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |

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Term	Documents
ALIGN$5	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	216935
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13449
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	250
ALIGNABS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNACE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
(L1 AND ALIGN$5 NEAR5 VECTOR$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	30

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☐   21.   Document ID: US 5664134 A

L2: Entry 21 of 30   File: USPT   Sep 2, 1997

US-PAT-NO: 5664134
DOCUMENT-IDENTIFIER: US 5664134 A

TITLE: Data processor for performing a comparison instruction using selective
enablement and wired boolean logic

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐   22.   Document ID: US 5600846 A

L2: Entry 22 of 30   File: USPT   Feb 4, 1997

US-PAT-NO: 5600846
DOCUMENT-IDENTIFIER: US 5600846 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐   23.   Document ID: US 5598571 A

L2: Entry 23 of 30   File: USPT   Jan 28, 1997

US-PAT-NO: 5598571
DOCUMENT-IDENTIFIER: US 5598571 A

TITLE: Data processor for conditionally modifying extension bits in response to data
processing instruction execution

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
| Draw Desc | Image |

☐   24.   Document ID: US 5572689 A

L2: Entry 24 of 30   File: USPT   Nov 5, 1996

US-PAT-NO: 5572689
DOCUMENT-IDENTIFIER: US 5572689 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐ 25.   Document ID: US 5559973 A

L2: Entry 25 of 30   File: USPT   Sep 24, 1996

US-PAT-NO: 5559973
DOCUMENT-IDENTIFIER: US 5559973 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐ 26.   Document ID: US 5548768 A

L2: Entry 26 of 30   File: USPT   Aug 20, 1996

US-PAT-NO: 5548768
DOCUMENT-IDENTIFIER: US 5548768 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐ 27.   Document ID: US 5537562 A

L2: Entry 27 of 30   File: USPT   Jul 16, 1996

US-PAT-NO: 5537562
DOCUMENT-IDENTIFIER: US 5537562 A

TITLE: Data processing system and method thereof

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
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☐ 28.   Document ID: US 5513366 A

L2: Entry 28 of 30   File: USPT   Apr 30, 1996

US-PAT-NO: 5513366
DOCUMENT-IDENTIFIER: US 5513366 A

TITLE: Method and system for dynamically reconfiguring a register file in a vector processor

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐   29.   Document ID: US 6266758 B1

L2: Entry 29 of 30   File: DWPI   Jul 24, 2001

DERWENT-ACC-NO: 2001-615271
DERWENT-WEEK: 200171
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TITLE: Aligning and ordering of vector components for single instruction multiple data processing, involves replicating extracted width vector into third register with components aligned for SIMD processing

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐   30.   Document ID: US 5933650 A

L2: Entry 30 of 30   File: DWPI   Aug 3, 1999

DERWENT-ACC-NO: 1999-443746
DERWENT-WEEK: 200172
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TITLE: Aligned vector element generating method for single instruction multiple data processing in computer system

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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Term	Documents
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ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13449
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	250
ALIGNABS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
(L1 AND ALIGN$5 NEAR5 VECTOR$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	30

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ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13449
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	250
ALIGNABS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNACE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
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(L1 AND ALIGN$5 NEAR5 VECTOR$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	30

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<u>L1</u>	simd and load$3 near5 vector$	173	<u>L1</u>

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1 **Comparison of dielectric properties determined from a computationa approach and experiment for anisotropic and periodic heterostructure**
Beroual, A.; Brosseau, C.
Dielectrics and Electrical Insulation, IEEE Transactions on [see also Electrical IEEE Transactions on] , Volume: 8 Issue: 6 , Dec. 2001
Page(s): 921 -929

[Abstract]   [PDF Full-Text (817 KB)] **JNL**

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2 **Size dependence of the magnetization vector reversal processes in e Fe[001] microstripes**
Ahmad, E.; Bland, J.A.C.; Gu, E.
Magnetics, IEEE Transactions on , Volume: 34 Issue: 4 Part: 1 , July 1998
Page(s): 1099 -1101

[Abstract]   [PDF Full-Text (460 KB)] **JNL**

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3 **A 25 k-gate BDCFL G/A with a differential push-pull ECL I/O**
Kaneko, Y.; Shimizu, H.; Nagata, K.; Koyanagi, M.; Okamoto, M.; Suzuki, M.; Yokokawa, S.; Shimizu, S.; Maejima, T.; Wada, J.; Kawada, H.; Ueno, S.; Min M.; Yaegashi, I.
Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 1993. Technical D 1993., 15th Annual , Oct. 1993
Page(s): 141 -144

[Abstract]   [PDF Full-Text (308 KB)] **CNF**

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4 **Auroral optical observations of plasma processes**
Murphree, J.S.

Plasma Science, 1990: IEEE Conference Record - Abstracts., 1990 IEEE Intern
Conference on , 1990
Page(s): 88

[Abstract]   [PDF Full-Text (96 KB)] **CNF**

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5 **Shape based classification of left ventricles by spectral analysis of th geometrical cardiograms**
Azhari, H.; Beyar, R.; Marcus, M.L.; Sideman, S.
Computers in Cardiology 1989, Proceedings. , 1990
Page(s): 87 -89

[Abstract]   [PDF Full-Text (172 KB)] **CNF**

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6 **Fast Fourier transforms for k-space computation**
Chou, F.-M.L.
Antennas and Propagation Society International Symposium, 1988. AP-S. Dig

Page(s): 88 -91 vol.1

[Abstract]   [PDF Full-Text (124 KB)] **CNF**

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